

800G Twin-port 2x400Gb/s OSFP to 2x400Gb/s QSFP112 Passive Copper Splitter Cable

Features

- 800Gb/s to 2x400Gb/s data rates
- Based on 100G-PAM4 modulation
- up to 3 meter lengths
- OSFP and QSFP112 ends each consume 0.1Watts
- Operating case temperature 0-70°C
- Hot pluggable
- RoHS compliant
- LF (Lead Free) HF (Halogen Free) PCB
- OSFP head end is CMIS based
- QSFP112 ends are SFF-8636 based

Applications

- 2x400G OSFP Ethernet switch-to-two 400Gb/s QSFP112 adapters

Description

O2Q112-800G-DAC is an 800Gb/s Twin-port OSFP (Octal Small Form-factor Pluggable) to 2x 400Gb/s QSFP112 (Quad Small Form-factor Pluggable 112G) passive Direct Attach Copper (DAC) dual breakout (aka splitter) cable for 400Gb/s End-to-End 800G solutions. The 8-channel Twin port OSFP end uses a finned top form-factor for use in 800G OSFP switch cages. The two 400G ends support 4-channels of 100G-PAM4 (400G) and use a flat top QSFP112 for use in 400G QSFP112 adapters using riding heat sinks on the connector cage. DAC cables are the lowest-cost, lowest-latency, near zero power consuming high-speed links available due to their simplicity of design and minimal components.

Absolute Maximum Specifications

Absolute maximum ratings are those beyond which damage to the device may occur.

Prolonged operation between the operational specifications and absolute maximum ratings is not intended and may cause permanent device degradation.

Table1-Absolute Maximum Specifications

Parameter	Min.	Typical	Max.	Unit	Note
Storage Temperature	-40		+85	°C	
Operating Case Temperature	0		70	°C	
Supply voltage	3.135	3.3	3.465	V	
Data input voltage	-0.3		3.6	V	
Control input voltage	-0.3		3.6	V	
Power Consumption			0.1	W	
Relative Humidity (non- condensing)	5		85	%	

Electrical Specifications

Table2-Electrical Specifications

Parameter	Symbol	Min	Typical	Max.	Units	Note
Characteristic impedance		90	100	110	Ω	
Time propagation delay		-	-	4.5	ns/m	

Mechanical Performance Requirements

Table3-Mechanical Performance Requirements

Parameter	Min.	Typical
Diameter	30AWG: 7.2 \pm 0.03 26AWG: 8.9 \pm 0.03	mm
Length tolerance	length < 2 m, \pm 25 length \geq 2 m, \pm 50	mm

Minimum Bend Radius

Table4-Minimum Bend Radius

OPN	Length (m)	AWG (mm)
O2Q112-800G-CU1	1.0	30AWG, 2x8pairs
O2Q112-800G-CUB	1.5	30AWG, 2x8pairs
O2Q112-800G-CU2	2.0	26AWG, 2x8pairs
O2Q112-800G-CUC	2.5	26AWG, 2x8pairs
O2Q112-800G-CU3	3	26AWG, 2x8pairs

Notes: The minimum assembly bending radius (close to the connector) is 10x the cable's outer diameter.

The repeated bend (far from the connector) is also 10x the cable's outer diameter. The single bend (far from the connector) is 5x the cable's outer diameter.

OSFP Pin Definition

Table5-OSFP Pin Definition

Pin	Symbol	Description	Pin	Symbol	Description
1	GND	Ground	31	GND	Ground
2	Tx2p	Transmitter Non-Inverted Data Input	32	Rx2p	Receiver Non-Inverted Data Output
3	Tx2n	Transmitter Inverted Data Input	33	Rx2n	Receiver Inverted Data Output
4	GND	Ground	34	GND	Grounds
5	Tx4p	Transmitter Non-Inverted Data Input	35	Rx4p	Receiver Non-Inverted Data Output
6	Tx4n	Transmitter Inverted Data Input	36	Rx4n	Receiver Inverted Data Output
7	GND	Ground	37	GND	Ground
8	Tx6p	Transmitter Non-Inverted Data Input	38	Rx6p	Receiver Non-Inverted Data Output
9	Tx6n	Transmitter Inverted Data Input	39	Rx6n	Receiver Inverted Data Output
10	GND	Ground	40	GND	Ground
11	Tx8p	Transmitter Non-Inverted Data input	41	Rx8p	Receiver Non-Inverted Data Output
12	Tx8n	Transmitter Inverted Data Input	42	Rx8n	Receiver Inverted Data Output
13	GND	Ground	43	GND	Ground
14	SCL	2-wire serial interface clock	44	INT / RSTn	Module Interrupt / Module Reset

15	VCC	+3.3V Power	45	VCC	+3.3V Power
16	VCC	+3.3V Power	46	VCC	+3.3V Power
17	LPWn / PRSn	Low-Power Mode / Module Present	47	SDA	2-wire Serial interface data
18	GND	Ground	48	GND	Ground
19	Rx7n	Receiver Inverted Data Output	49	Tx7n	Transmitter Inverted Data Input
20	Rx7p	Receiver Non-Inverted Data Output	50	Tx7p	Transmitter Non-Inverted Data Input
21	GND	Ground	51	GND	Ground
22	Rx5n	Receiver Inverted Data Output	52	Tx5n	Transmitter Inverted Data Input
23	Rx5p	Receiver Non-Inverted Data Output	53	Tx5p	Transmitter Non-Inverted Data Input
24	GND	Ground	54	GND	Ground
25	Rx3n	Receiver Inverted Data Output	55	Tx3n	Transmitter Inverted Data Input
26	Rx3p	Receiver Non-Inverted Data Output	56	Tx3p	Transmitter Non-Inverted Data Input
27	GND	Ground	57	GND	Ground
28	Rx1n	Receiver Inverted Data Output	58	Tx1n	Transmitter Inverted Data Input
29	Rx1p	Receiver Non-Inverted Data Output	59	Tx1p	Transmitter Non-Inverted Data Input
30	GND	Ground	60	GND	Ground

OSFP Module Pad Layout

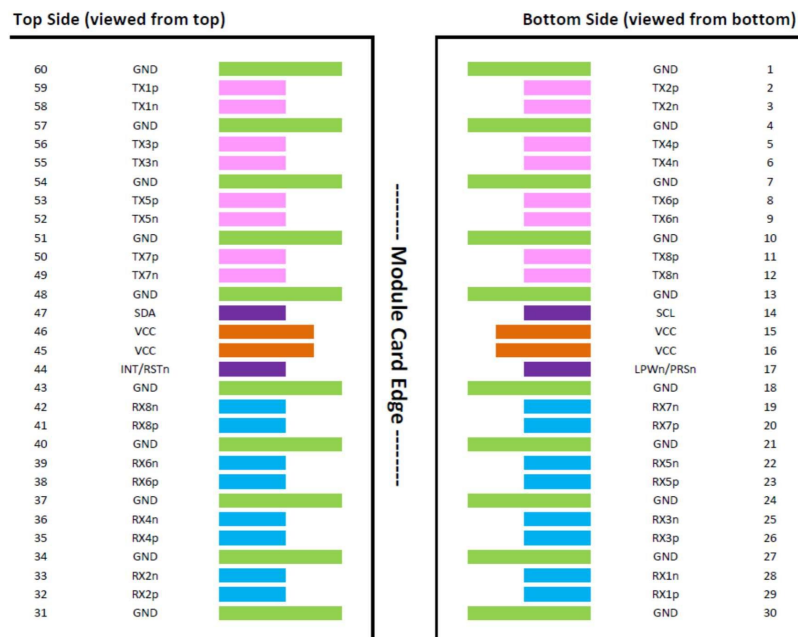


Figure 1 OSFP Module Pad Layout

QSFP112 Pin Definition

Table6-QSFP112 Pin Definition

Pin	Symbol	Description	Pin	Symbol	Description
1	Ground	Ground	20	Ground	Ground
2	Tx2n	Connected to Port 1 lane Rx2 Inverted Data	21	Rx2n	Connected to Port 1 lane Tx2 Inverted Data
3	Tx2p	Connected to Port 1 lane Rx2 Non-Inverted Data	22	Rx2p	Connected to Port 1 lane Tx2 Non-Inverted Data
4	Ground	Ground	23	Ground	Grounds
5	Tx4n	Connected to Port 2 lane Rx2 Non-Inverted Data	24	Rx4n	Connected to Port 2 lane Tx2 Inverted Data

6	Tx4p	Connected to Port 2 lane Rx2 Inverted Data	25	Rx4p	Connected to Port 2 lane Tx2 Non-Inverted Data
7	Ground	Ground	26	Ground	Ground
8	Mod-SelL	Cable Select	27	ModPrsL	Cable Present
9	ResetL	Cable Reset	28	IntL	Interrupt
10	Vcc Rx	+3.3V Power supply receiver	29	Vcc Tx	+3.3V Power supply transmitter
11	SCL	2-wire serial interface clock	30	Vcc1	+3.3V Power Supply
12	SDA	2-wire serial interface data	31	LPMode	Low Power Mode
13	Ground	Ground	32	Ground	Ground
14	Rx3p	Connected to Port 2 lane Tx1 Non-Inverted Data	33	Tx3p	Connected to Port 2 lane Rx1 Non-Inverted Data
15	Rx3n	Connected to Port 2 lane Tx1 Inverted Data	34	Tx3n	Connected to Port 2 lane Rx1 Inverted Data
16	Ground	Ground	35	Ground	Ground
17	Rx1p	Connected to Port 1 lane Tx1 Non-Inverted Data	36	Tx1p	Connected to Port 1 lane Rx1 Non-Inverted Data
18	Rx1n	Connected to Port 1 lane Tx1 Inverted Data	37	Tx1n	Connected to Port 1 lane Rx1 Inverted Data
19	Ground	Ground	38	Ground	Ground

QSFP112 Module Pad Layout

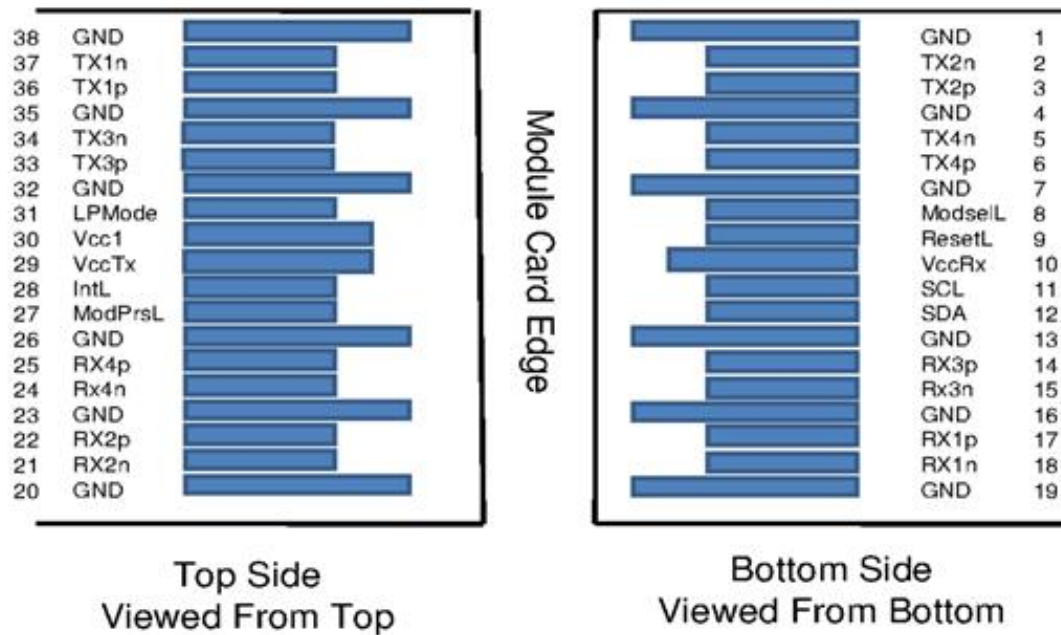


Figure 2 QSFP112 Module Pad Layout

Assembly Bending Radius

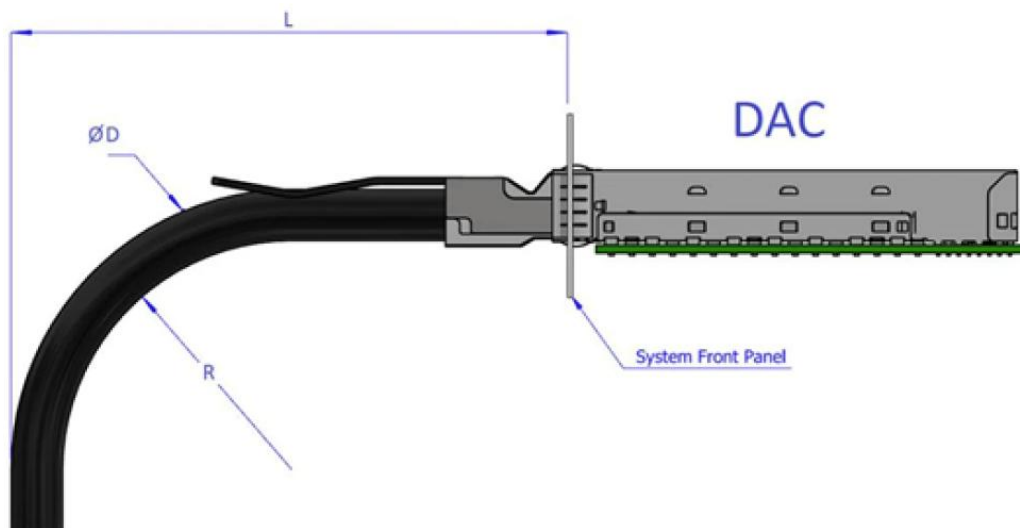


Figure 3 Assembly Bending Radius

Cable Dimensions

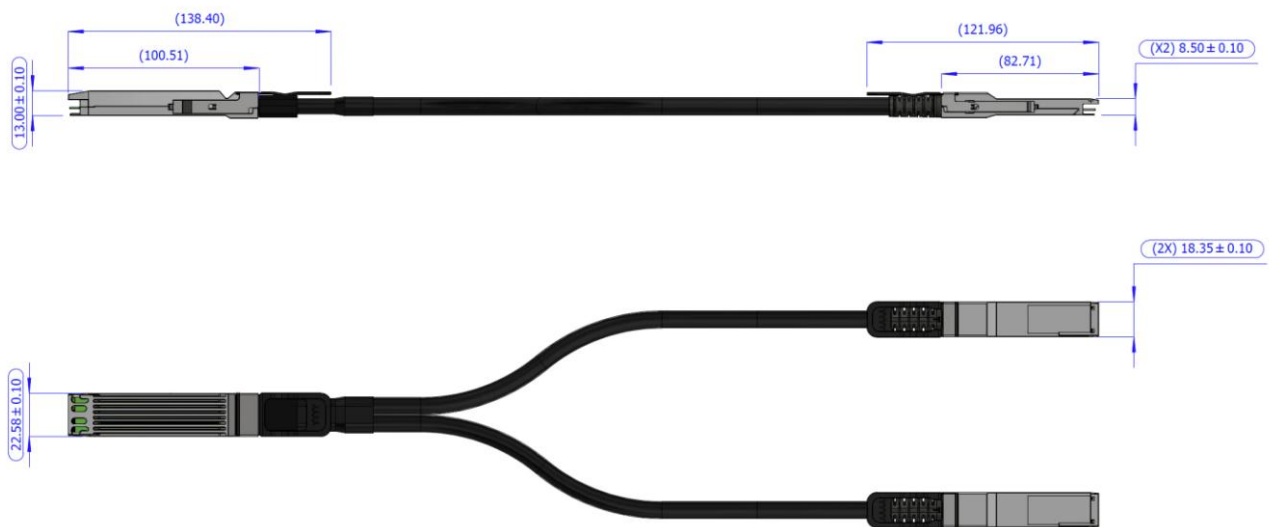


Figure 4 Cable Dimensions

OSFP Finned Head Dimensions

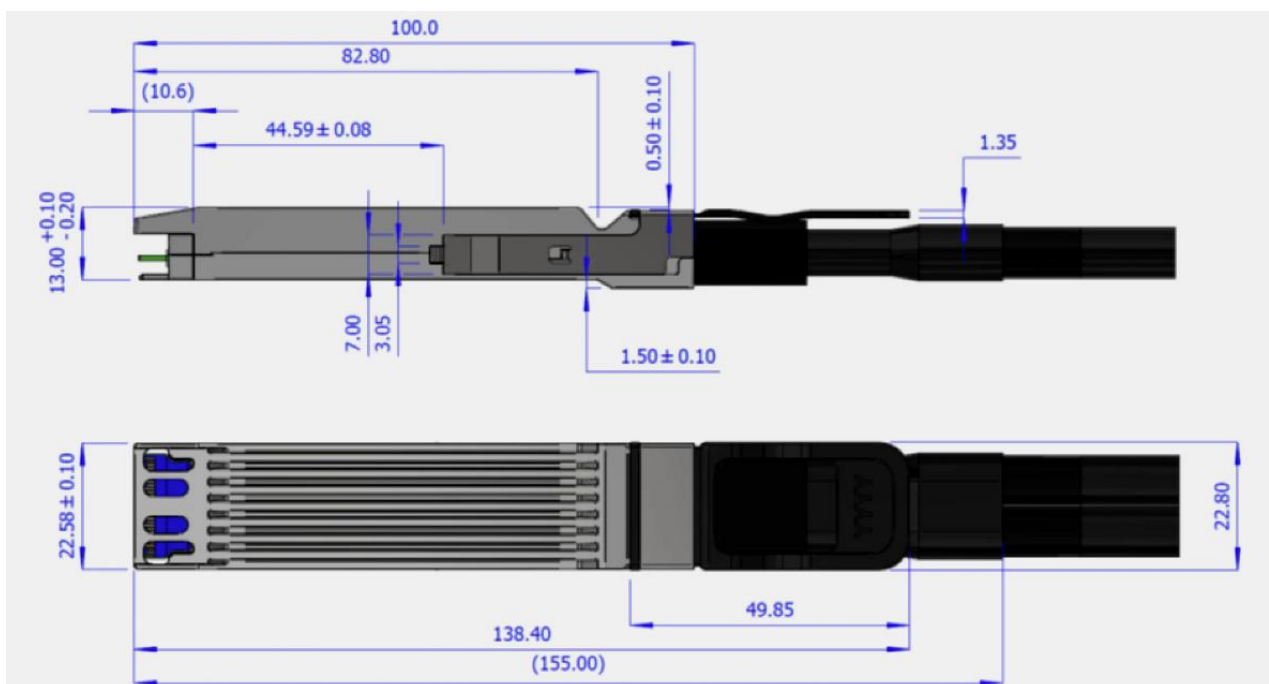


Figure 5 OSFP Finned Head Dimensions

QSFP112 Flat Ends Dimensions

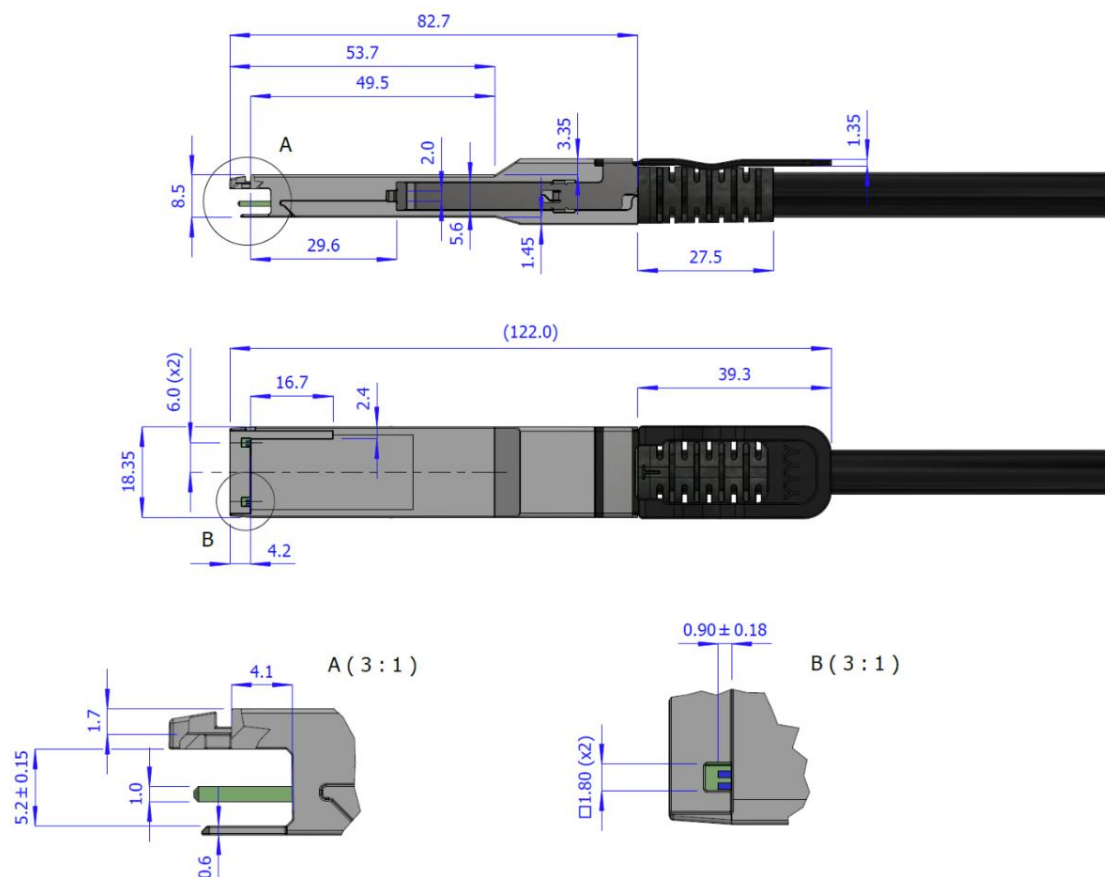


Figure 6 QSFP112 Flat Head Dimensions

Ordering Information

OPN	Description
O2Q112-800G-CU1	1m (3ft) Twin-port 2x400Gb/s OSFP Finned Top to 2x400Gb/s QSFP112 Flat Top Passive Copper Splitter Cable
O2Q112-800G-CUB	1.5m (5ft) Twin-port 2x400Gb/s OSFP Finned Top to 2x400Gb/s QSFP112 Flat Top Passive Copper Splitter Cable
O2Q112-800G-CU2	2m (7ft) Twin-port 2x400Gb/s OSFP Finned Top to 2x400Gb/s QSFP112 Flat Top Passive Copper Splitter Cable
O2Q112-800G-CUC	2.5m (8ft) Twin-port 2x400Gb/s OSFP Finned Top to 2x400Gb/s QSFP112 Flat Top Passive Copper Splitter Cable

O2Q112-800G-CU3	3m (10ft) Twin-port 2x400Gb/s OSFP Finned Top to 2x400Gb/s QSFP112 Flat Top Passive Copper Splitter Cable
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PNY Technologies Europe
9 rue Joseph Cugnot
33708 Mérignac cedex | France
T +33 (0)5 40 240 240 | pnipro@pny.eu

I Further Information

Web www.naddod.com

Email For order requirements: sales@naddod.com

For other information: info@naddod.com

For cooperation: agency@naddod.com

For technical support: tech@naddod.com

For customer service: support@naddod.com

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