

400G OSFP(Finned) to 2x200Gb/s QSFP56 HDR Passive Copper Splitter Cable

Features

- Compatible with IEEE 802.3cd
- Supports aggregate data rates of 2X200Gbps(PAM4)
- Optimized construction to minimize insertion loss and crosstalk
- Pull-to-release slide latch design
- Straight and break out assembly configurations available
- Customized cable braid termination limits EMI radiation
- Customizable EEPROM mapping for cable signature
- 26AWG and 30AWG cable
- 3.3V Power supply
- Temperature Range: 0~ 70 °C
- RoHs Compliant

Product Applications

- Switches, servers and routers
- Data Center networks
- Storage area networks
- High performance computing
- Telecommunication and wireless infrastructure
- Medical diagnostics and networking
- Test and measurement equipment

Industry Standards

- InfiniBand HDR
- 2x200G Ethernet(IEEE 802.3cd)

Description

O2Q56-400G-DACH passive copper Splitter cable assembly feature sixteen differential copper pairs, providing eight data transmission channels at speeds up to 56Gbps(PAM4) per channel, and meets 2X200G Ethernet and InfiniBand High Data Rate(HDR) requirements. Available in 26AWG and 30AWG wire gauges, this 400G OSFP to 2X200G QSFP56 copper cable assembly features low insertion loss and low crosstalk.

400G OSFP to 2X200G QSFP56 passive copper cable uses PAM4 signals for transmission, which doubles the rate. However, there are more stringent requirements for cable insertion loss. For detailed requirements, please see High Speed Characteristics.

Absolute Maximum Specifications

Absolute maximum ratings are those beyond which damage to the device may occur.

Prolonged operation between the operational specifications and absolute maximum ratings is not intended and may cause permanent device degradation.

Table1-Absolute Maximum Specifications

Parameter	Min.	Max.	Unit
Supply voltage	-0.3	3.6	V
Data input voltage	-0.3	3.6	V
Control input voltage	-0.3	3.6	V

Environmental Specifications

This table shows the environmental specifications for the product

Table2-Environmental Specifications

Parameter	Min	Typical	Max.	Units
Storage Temperature	-40		85	°C

Operational Specifications

This section shows the range of values for normal operation. The host board power supply filtering should be designed as recommended in the SFF Committee Spec.

Table3-Optical Specifications				
Parameter	Min.	Typical	Max.	Units
Supply voltage (Vcc)	3.135	3.3	3.465	V
Power Consumption	--	---	0.1	W
Operating case temperature	0	---	70	°C
Operating relative humidity	5	---	85	%

High Speed Characteristics

Table4-High Speed Characteristics					
Parameter	Min.	Typical	Max	Units	Note
Differential Impedance	90	100	110	Ω	
Insertion loss	-17.16			dB	At 13.28 GHz
Differential Return Loss			See 1	dB	At 0.05 to 4.1 GHz
			See 2	dB	At 4.1 to 19 GHz
Common-mode to common-mode output return loss			-2	dB	At 0.2 to 19 GHz
Differential to common-mode return loss			See 3	dB	At 0.01 to 12.89 GHz
			See 4		At 12.89 to 19 GHz
Differential to common Mode Conversion Loss			-10	dB	At 15.7 to 19 GHz
			See 5		
			-6.3		

Notes:

- [1] Reflection Coefficient given by equation $SDD11(dB) < -16.5 + 2 \times \sqrt{f}$, with f in GHz
- [2] Reflection Coefficient given by equation $SDD11(dB) < -10.66 + 14 \times \log_{10}(f/5.5)$, with f in GHz
- [3] Reflection Coefficient given by equation $SCD11(dB) < -22 + (20/25.78)*f$, with f in GHz
- [4] Reflection Coefficient given by equation $SCD11(dB) < -15 + (6/25.78)*f$, with f in GHz
- [5] Reflection Coefficient given by equation $SCD21(dB) < -27 + (29/22)*f$, with f in GHz

Regulatory Compliance

Table5-Regulatory Compliance		
Feature	Test Method	Performance
Electrostatic Discharge (ESD) to the Electrical Pins	MIL-STD-883C Method 3015.7	Class 1(>2000 Volts)

Electromagnetic Interference(EMI)	FCC Class B	Compliant with Standards
	CENELEC EN55022 Class B	
	CISPR22 ITE Class B	
RF Immunity(RFI)	IEC61000-4-3	Typically Show no Measurable Effect from a 10V/m Field Swept from 80 to 1000MHz
RoHS Compliance	RoHS Directive 2011/65/EU and it's Amendment Directives (EU) 2015/863	RoHS (EU) 2015/863 compliant
REACH Compliance	REACH Regulation (EC) No 1907/2006	REACH (EC) No 1907/2006 compliant
Electrostatic Discharge (ESD) to the Electrical Pins	MIL-STD-883C Method 3015.7	Class 1(>2000 Volts)
Electromagnetic Interference(EMI)	FCC Class B	Compliant with Standards
	CENELEC EN55022 Class B	

Minimum Bend Radius

Table6-Minimum Bend Radius

OPN	Length (m)	AWG (mm)
O2Q56-400G-CUAH	0.50	30AWG, 2x8pairs
O2Q56-400G-CU1H	1.0	30AWG, 2x8pairs
O2Q56-400G-CUBH	1.5	30AWG, 2x8pairs
O2Q56-400G-CU2H	2.0	26AWG, 2x8pairs
O2Q56-400G-CU3H	3.0	26AWG, 2x8pairs

Pin Description

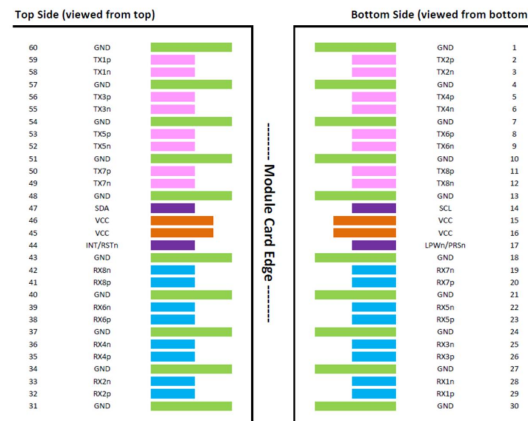
The DAC is OSFP MSA Specification for OSFP Octal Small Form Factor Pluggable Module Rev. 1.12 compliant, see www.osfpmsa.org.

Table7-OSFP Pin Description

Pin	Symbol	Description	Pin	Symbol	Description
1	GND	Ground	31	GND	Ground
2	Tx2p	Transmitter Non-Inverted Data Input	32	Rx2p	Receiver Non-Inverted Data Output
3	Tx2n	Transmitter Inverted Data Input	33	Rx2n	Receiver Inverted Data Output
4	GND	Ground	34	GND	Grounds
5	Tx4p	Transmitter Non-Inverted Data Input	35	Rx4p	Receiver Non-Inverted Data Output

6	Tx4n	Transmitter Inverted Data Input	36	Rx4n	Receiver Inverted Data Output
7	GND	Ground	37	GND	Ground
8	Tx6p	Transmitter Non-Inverted Data Input	38	Rx6p	Receiver Non-Inverted Data Output
9	Tx6n	Transmitter Inverted Data Input	39	Rx6n	Receiver Inverted Data Output
10	GND	Ground	40	GND	Ground
11	Tx8p	Transmitter Non- Inverted Data input	41	Rx8p	Receiver Non-Inverted Data Output
12	Tx8n	Transmitter Inverted Data Input	42	Rx8n	Receiver Inverted Data Output
13	GND	Ground	43	GND	Ground
14	SCL	2-wire serial interface clock	44	INT / RSTn	Module Interrupt / Module Reset
15	VCC	+3.3V Power	45	VCC	+3.3V Power
16	VCC	+3.3V Power	46	VCC	+3.3V Power
17	LPWn / PRSn	Low-Power Mode / Module Present	47	SDA	2-wire Serial interface data
18	GND	Ground	48	GND	Ground
19	Rx7n	Receiver Inverted Data Output	49	Tx7n	Transmitter Inverted Data Input
20	Rx7p	Receiver Non-Inverted Data Output	50	Tx7p	Transmitter Non-Inverted Data Input
21	GND	Ground	51	GND	Ground
22	Rx5n	Receiver Inverted Data Output	52	Tx5n	Transmitter Inverted Data Input
23	Rx5p	Receiver Non-Inverted Data Output	53	Tx5p	Transmitter Non-Inverted Data Input
24	GND	Ground	54	GND	Ground
25	Rx3n	Receiver Inverted Data Output	55	Tx3n	Transmitter Inverted Data Input
26	Rx3p	Receiver Non-Inverted Data Output	56	Tx3p	Transmitter Non-Inverted Data Input
27	GND	Ground	57	GND	Ground
28	Rx1n	Receiver Inverted Data Output	58	Tx1n	Transmitter Inverted Data Input
29	Rx1p	Receiver Non-Inverted Data Output	59	Tx1p	Transmitter Non-Inverted Data Input
30	GND	Ground	60	GND	Ground

OSFP Module Pad Layout

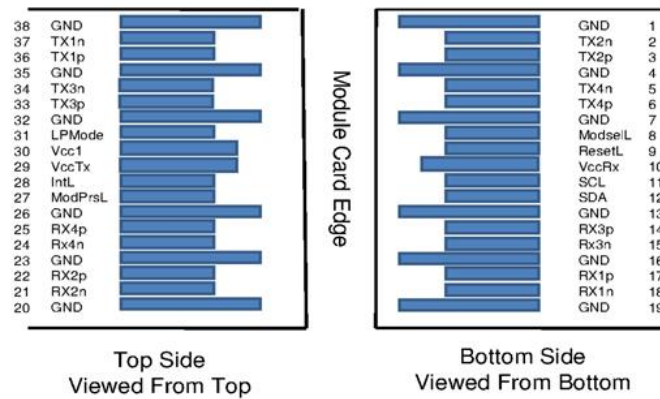


QSFP56 Pin Description 200Gb/s End

Pin	Symbol	Description	Pin	Symbol	Description
1	Ground	Ground	20	Ground	Ground
2	Tx2n	Connected to Port 1 lane Rx2 Inverted Data	21	Rx2n	Connected to Port 1 lane Tx2 Inverted Data
3	Tx2p	Connected to Port 1 lane Rx2 Non-Inverted Data	22	Rx2p	Connected to Port 1 lane Tx2 Non-Inverted Data
4	Ground	Ground	23	Ground	Grounds
5	Tx4n	Connected to Port 2 lane Rx2 Non-Inverted Data	24	Rx4n	Connected to Port 2 lane Tx2 Inverted Data
6	Tx4p	Connected to Port 2 lane Rx2 Inverted Data	25	Rx4p	Connected to Port 2 lane Tx2 Non-Inverted Data
7	Ground	Ground	26	Ground	Ground
8	Mod-SelL	Cable Select	27	ModPrsL	Cable Present
9	ResetL	Cable Reset	28	IntL	Interrupt
10	Vcc Rx	+3.3V Power supply receiver	29	Vcc Tx	+3.3V Power supply transmitter
11	SCL	2-wire serial interface clock	30	Vcc1	+3.3V Power Supply
12	SDA	2-wire serial interface data	31	LPMODE	Low Power Mode
13	Ground	Ground	32	Ground	Ground
14	Rx3p	Connected to Port 2 lane Tx1 Non-Inverted Data	33	Tx3p	Connected to Port 2 lane Rx1 Non-Inverted Data
15	Rx3n	Connected to Port 2 lane Tx1 Inverted Data	34	Tx3n	Connected to Port 2 lane Rx1 Inverted Data
16	Ground	Ground	35	Ground	Ground
17	Rx1p	Connected to Port 1 lane Tx1 Non-Inverted Data	36	Tx1p	Connected to Port 1 lane Rx1 Non-Inverted Data
18	Rx1n	Connected to Port 1 lane Tx1 Inverted Data	37	Tx1n	Connected to Port 1 lane Rx1 Inverted Data

		Tx1 Inverted Data			Rx1 Inverted Data
19	Ground	Ground	38	Ground	Ground

QSFP56 Module Pad Layout



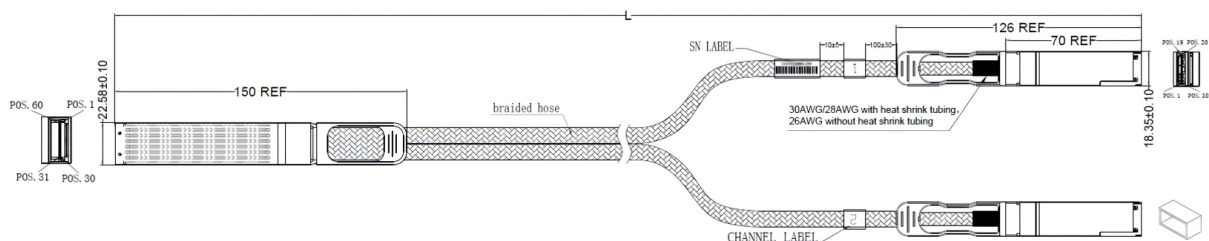
Ordering Information

Table9-Ordering Information

PN	Description
O2Q56-400G-CUAH	400Gb/s OSFP to 2x200Gb/s QSFP56 HDR Passive Copper Splitter Cable,0.5m,finned top
O2Q56-400G-CU1H	400Gb/s OSFP to 2x200Gb/s QSFP56 HDR Passive Copper Splitter Cable,1m,finned top
O2Q56-400G-CUBH	400Gb/s OSFP to 2x200Gb/s QSFP56 HDR Passive Copper Splitter Cable,1.5m,finned top
O2Q56-400G-CU2H	400Gb/s OSFP to 2x200Gb/s QSFP56 HDR Passive Copper Splitter Cable,2m,finned top
O2Q56-400G-CU3H	400Gb/s OSFP to 2x200Gb/s QSFP56 HDR Passive Copper Splitter Cable,3m,finned top

Mechanical Dimensions

The connector is compatible with the SFF8024 (OSFP) and SFF8672 (QSFP56) specification.



Further Information:

Web www.naddod.com

Email For order requirements: sales@naddod.com
For customer service: support@naddod.com
For technical support: tech@naddod.com

For cooperation: agency@naddod.com

For other information: info@naddod.com

Disclaimer

1. We are committed to continuous product improvement and feature upgrades, and the contents contained in this manual are subject to change without notice.
2. Nothing herein should be construed as constituting an additional warranty.
3. NADDOD assumes no responsibility for the use or reliability of equipment or software not provided by NADDOD.

Copyright © NADDOD.COM All Rights

The logo for PNY, featuring the letters 'PNY' in a stylized, bold, white font with a registered trademark symbol.

PNY Technologies Europe
9 rue Joseph Cugnot
33708 Mérignac cedex | France
T +33 (0)5 40 240 240 | pnipro@pny.eu

NADDOD - Building an Intelligent World with Everything Connected
Accelerated AI Clusters | HPC Datacenter | Enterprise Networking