

800G Twin-port 2x400Gb/s OSFP to 2x400Gb/s OSFP Passive Copper Cable

Features

- 2x400Gb/s data rate
- Based on 8-channels of 100G-PAM4 modulation
- 0.5, 1, 1.5, 2, 3 meter lengths
- 0.1 Watts max per end Operate
- Finned Top to Finned Top
- Operating case temperature 0-70°C
- Single 3.3V supply voltage
- Hot pluggable
- RoHS compliant
- polyvinylchloride (PVC) jacket
- LF (Lead Free) HF (Halogen Free) PCB
- SFF-8636/SFF-8665/OSFP msa.org based

Applications

- 2x400Gb/s OSFP Ethernet switch-to-switch

Description

OSFP-800G-DAC is an 2x400Gb/s twin-port OSFP (Octal Small Form factor Pluggable) to 2x400Gb/s twin-port OSFP Direct Attached Copper cable (DAC). DAC cables are the lowest-cost, lowest-latency, near zero power consuming, high-speed links available due to their simplicity of design and minimal components. Using the Octal Small Form factor Plug (OSFP) and Containing eight high-speed electrical copper pairs, each operating at data rates of up to 100Gb/s. NADDOD's cable solutions provide power-efficient connectivity enabling higher port bandwidth, density and configurability at a low cost and reduced power requirement in the data centers. Rigorous cable production testing ensures best out-of-the-box installation experience, performance, and durability.

Absolute Maximum Specifications

Absolute maximum ratings are those beyond which damage to the device may occur.

Prolonged operation between the operational specifications and absolute maximum ratings is not intended and may cause permanent device degradation.

Table1-Absolute Maximum Specifications

Parameter	Min.	Typical	Max.	Unit	Note
Storage Temperature	-40		+85	°C	
Operating Case Temperature	0		70	°C	
Supply voltage	3.135	3.3	3.465	V	
Data input voltage	-0.3		3.6	V	
Control input voltage	-0.3		3.6	V	
Power Consumption			0.1	W	
Relative Humidity (non- condensing)	5		85	%	

Electrical Specifications

Table2-Electrical Specifications

Test Items	Test Condition	Specification
Current		0.5A per contact
Voltage		30 vDC per contact
LLCR	EIA 364-23, 20mVdc, 100mA	less than 2 ohms.
Continuity	Verify the continuous electrical path	No open, short, or high resistance.

SI Requirements

Table3-SI Requirements

Test Items	Specification	Notes
SDD21&SDD12	≤ 19.75 dB Min. @26.56 GHz; ≥ 11.0 dB max. @26.56GHz;	From 0.01 GHz to 26.56GHz
ERL	Minimum cable assembly ERL(*) : ≥ 8.25 dB	

Mechanical Performance Requirements

Table4-Mechanical Performance Requirements

Test Items	Test Condition	Specification
Mating Forces	A rate of 10mm per minute	OSFP < 40N
Un-mating Forces	A rate of 10mm per minute	OSFP < 30N
Latch strength	Pull to separate module from cage,Test with connector, cage & module (latch	Minimum of an 125N force

	engaged)	
Bulk cable retention in module	Pull to separate bulk cable from module, Test with cable assembly only	Minimum of an 90N force
Wire Flex	Flex cable 180° for 10 cycles at X/Y axis, 20 times/minutes, with an 1kg suspended weight. Type C EIA 364-41, test condition I.	No microsecond discontinuities are allowed.
Durability	Perform 50 unplug/plugin cycles	No evidence of physical damage
Cable Minimum Bend Radius	The cable is bent on time over the correct mandrel with 5 perpendicular, the Minimum bend Radius is 10x OD.	No physical damage, Verify continuity and SI

Mechanical Specifications

Table5-Mechanical Specifications

Parameter	Value	Units
Diameter	30/28/26AWG	mm
Length tolerance	length < 2 m	±25mm
	length ≥ 2 m	±50mm

Minimum Bend Radius

Table6-Minimum Bend Radius

OPN	Length (m)	AWG (mm)
OSFP-800G-CUA	0.50	30AWG, 2x8pairs

OSFP-800G-CU1	1.0	28AWG, 2x8pairs
OSFP-800G-CUB	1.5	28AWG, 2x8pairs
OSFP-800G-CU2	2.0	28AWG, 2x8pairs
OSFP-800G-CU3	3.0	26AWG, 2x8pairs

OSFP Pin Definition

Table7-OSFP Pin Definition

Pin	Symbol	Description	Pin	Symbol	Description
1	GND	Ground	31	GND	Ground
2	Tx2p	Transmitter Non-Inverted Data Input	32	Rx2p	Receiver Non-Inverted Data Output
3	Tx2n	Transmitter Inverted Data Input	33	Rx2n	Receiver Inverted Data Output
4	GND	Ground	34	GND	Grounds
5	Tx4p	Transmitter Non-Inverted Data Input	35	Rx4p	Receiver Non-Inverted Data Output
6	Tx4n	Transmitter Inverted Data Input	36	Rx4n	Receiver Inverted Data Output
7	GND	Ground	37	GND	Ground
8	Tx6p	Transmitter Non-Inverted Data Input	38	Rx6p	Receiver Non-Inverted Data Output
9	Tx6n	Transmitter Inverted Data Input	39	Rx6n	Receiver Inverted Data Output
10	GND	Ground	40	GND	Ground
11	Tx8p	Transmitter Non-Inverted Data input	41	Rx8p	Receiver Non-Inverted Data Output

12	Tx8n	Transmitter Inverted Data Input	42	Rx8n	Receiver Inverted Data Output
13	GND	Ground	43	GND	Ground
14	SCL	2-wire serial interface clock	44	INT / RSTn	Module Interrupt / Module Reset
15	VCC	+3.3V Power	45	VCC	+3.3V Power
16	VCC	+3.3V Power	46	VCC	+3.3V Power
17	LPWn / PRSn	Low-Power Mode / Module Present	47	SDA	2-wire Serial interface data
18	GND	Ground	48	GND	Ground
19	Rx7n	Receiver Inverted Data Output	49	Tx7n	Transmitter Inverted Data Input
20	Rx7p	Receiver Non-Inverted Data Output	50	Tx7p	Transmitter Non-Inverted Data Input
21	GND	Ground	51	GND	Ground
22	Rx5n	Receiver Inverted Data Output	52	Tx5n	Transmitter Inverted Data Input
23	Rx5p	Receiver Non-Inverted Data Output	53	Tx5p	Transmitter Non-Inverted Data Input
24	GND	Ground	54	GND	Ground
25	Rx3n	Receiver Inverted Data Output	55	Tx3n	Transmitter Inverted Data Input
26	Rx3p	Receiver Non-Inverted Data Output	56	Tx3p	Transmitter Non-Inverted Data Input
27	GND	Ground	57	GND	Ground
28	Rx1n	Receiver Inverted Data Output	58	Tx1n	Transmitter Inverted Data Input
29	Rx1p	Receiver Non-Inverted Data Output	59	Tx1p	Transmitter Non-Inverted Data Input
30	GND	Ground	60	GND	Ground

OSFP Module Pad Layout

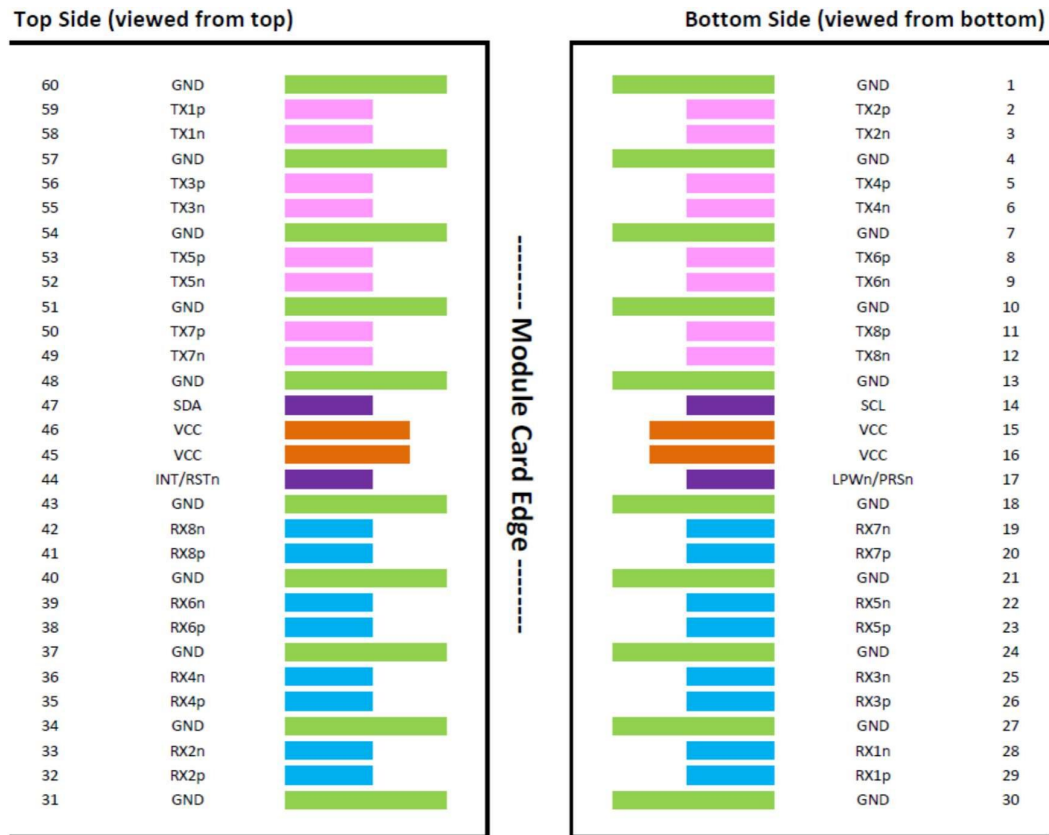


Figure 1 OSFP Module Pad Layout

Assembly Bending Radius

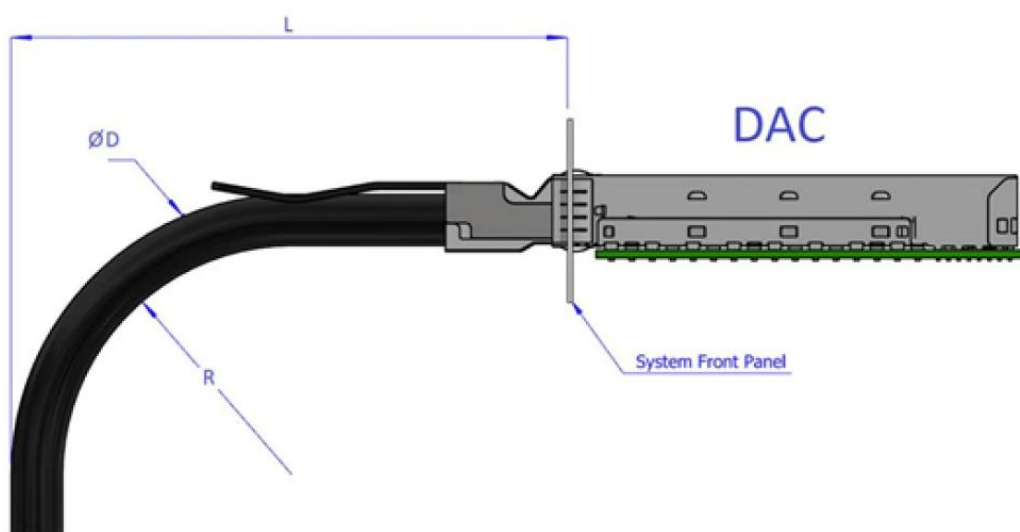


Figure 2 Assembly Bending Radius

OSFP Finned Head Dimensions

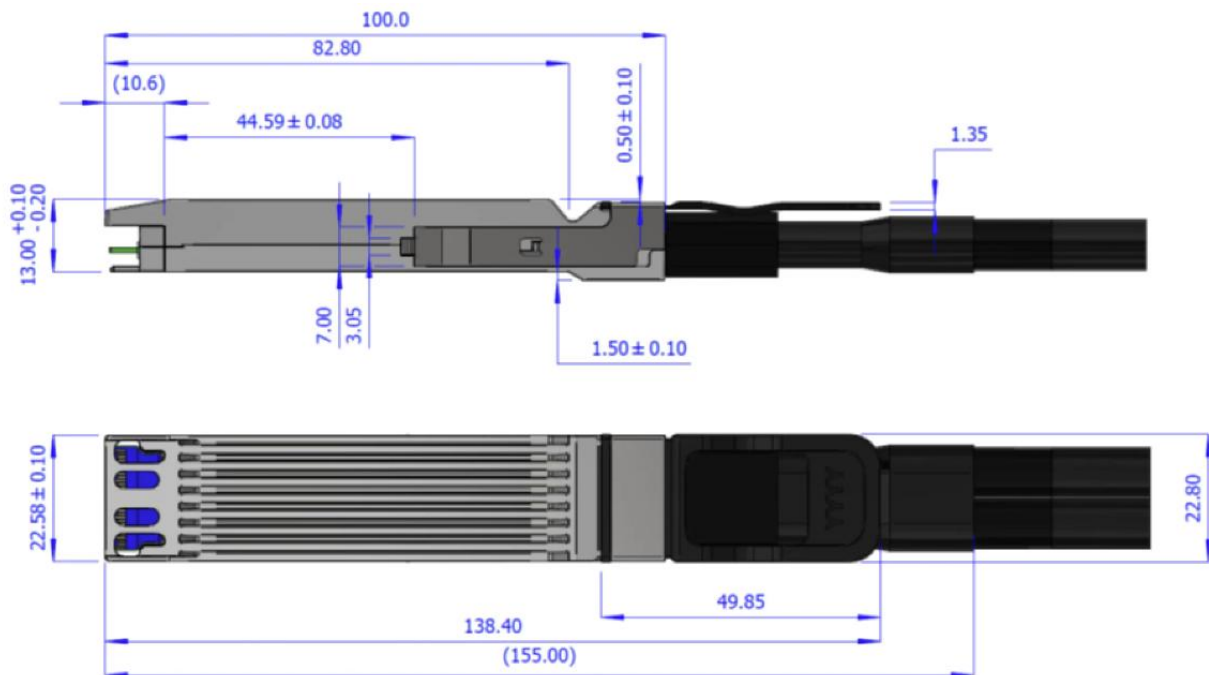


Figure 3 OSFP Finned Head Dimensions

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